IN THE CLAIMS:

Please amend claims 1, 9, 30 and 39 as indicated below. No new matter is believed to be introduced as a result of the aforementioned claim amendments.

1. **(Currently amended)** A integrated circuit configured to improve electrical signals in fiber-optic communication devices, the integrated circuit comprising:

a voltage filter adapted to receive an IC voltage from an external power supply and configured to generate a filtered voltage at a voltage level less than the IC voltage; and

a signal processing integrated circuit powered by the filtered voltage, the signal processing integrated circuit designed to be operated at the voltage level of the filtered voltage.

2. **(Withdrawn)** The integrated circuit of claim 1, wherein the signal processing circuit is a clock and data recovery circuit.

3. **(Withdrawn)** The integrated circuit of claim 1, wherein the signal processing circuit is an eye-opening circuit.

4. **(Original)** The integrated circuit of claim 1, wherein the voltage filter is a low-dropout linear filter.

5. **(Original)** The integrated circuit of claim 1, wherein the voltage filter is a passive filter.

6. **(Original)** The integrated circuit of claim 1, wherein the voltage filter is an active filter.

7. **(Original)** The integrated circuit of claim 1, wherein the voltage filter is adapted to filter noise in a range from about 10Khz to about 3Mhz.

8. **(Original)** The integrated circuit of claim 1, wherein the IC voltage is at least one of 1.8V, 3.3V and 5 V.

9. **(Currently amended)** The integrated circuit of claim 1, wherein the signal processing integrated circuit is an application specific integrated circuit.

10. **(Withdrawn)** A line card for use in fiber-optic communication devices, the line card comprising:

a connector adapted to couple the line card to a backplane for receiving at least one IC voltage;

a voltage filter on the line card and adapted to receive the IC voltage, the voltage filter configured to generate a filtered voltage at a voltage level less than the IC voltage; and

a signal processing integrated circuit powered by the filtered voltage, the signal processing integrated circuit designed to be operable at the voltage level of the filtered voltage.

11. **(Withdrawn)** The line card of claim 10, further comprising an LC filter coupled to the voltage filter.

12. **(Withdrawn)** The line card of claim 10, wherein the signal processing integrated circuit is a clock and data recovery circuit.

13. **(Withdrawn)** The line card of claim 10, wherein the signal processing integrated circuit is an eye-opening circuit.

14. **(Withdrawn)** The line card of claim 10, wherein the voltage filter is a low-dropout linear filter.

15. **(Withdrawn)** The line card of claim 10, wherein the voltage filter is a passive filter.

16. **(Withdrawn)** The line card of claim 10, wherein the voltage filter is an active filter.

17. **(Withdrawn)** The line card of claim 10, wherein the voltage filter is adapted to filter harmful noise in the range from about 10Khz to about 3Mhz.

18. **(Withdrawn)** The line card of claim 10, wherein the at least one IC voltage is at least one of 1.8V, 3.3V and 5 V.

19. **(Withdrawn)** The line card of claim 10, wherein the integrated circuit is an application specific integrated circuit.

20. **(Withdrawn)** A transceiver module usable in fiber-optic communication applications, the transceiver module comprising:

a connector adapted to receive at least one IC voltage;

a voltage filter that filters noise from the at least one IC voltage and that produces a filtered voltage at a voltage level less than the IC voltage; and

a signal processing integrated circuit powered by the filtered voltage, the signal processing integrated circuit designed to be operable at the voltage level of the filtered voltage.

21. **(Withdrawn)** The transceiver module of claim 20, further comprising an LC filter coupled to the voltage filter.

22. **(Withdrawn)** The transceiver module of claim 20, wherein the signal processing integrated circuit is a clock and data recovery circuit.

23. **(Withdrawn)** The transceiver module of claim 20, wherein the signal processing integrated circuit is an eye-opening circuit.

24. **(Withdrawn)** The transceiver module of claim 20, wherein the voltage filter is a low-dropout linear filter.

25. **(Withdrawn)** The transceiver module of claim 20, wherein the voltage filter is a passive filter.

26. **(Withdrawn)** The transceiver module of claim 20, wherein the voltage filter is an active filter.

27. **(Withdrawn)** The transceiver module of claim 20, wherein the voltage filter is adapted to filter harmful noise in the range from about 10Khz to about 3Mhz.

28. **(Withdrawn)** The transceiver module of claim 20, wherein the at least one IC voltage is at least one of 1.8V, 3.3V and 5 V.

29. **(Withdrawn)** The transceiver module of claim 20, wherein the integrated circuit is an application specific integrated circuit.

30. **(Currently amended)** A transceiver module usable in fiber-optic communication applications, the transceiver module comprising:

a receiver optical subassembly configured to receive optical signals;

a transmitter optical subassembly configured to transmit optical signals;

a connector adapted to couple the transceiver to an IC voltage associated with an external power supply;

~~a signal processing integrated circuit for processing the received optical signals and the transmitted optical signals the signal processing integrated circuit comprising~~ a voltage filter adapted to [[filter]] receive the IC voltage from the external power supply and generate a filtered voltage at a voltage level less than the IC voltage~~, portions of the signal processing integrated circuit designed to be operable at the filtered voltage~~; and

a signal processing integrated circuit powered by the filtered voltage, the signal processing integrated circuit designed to be operated at the voltage level of the filtered voltage.

31. **(Original)** The transceiver module of claim 30, further comprising an LC filter coupled to the voltage filter.

32. **(Withdrawn)** The transceiver module of claim 30, wherein the signal processing integrated circuit is a clock and data recovery circuit.

33. **(Withdrawn)** The transceiver module of claim 30, wherein the signal processing integrated circuit is an eye-opening circuit.

34. **(Original)** The transceiver module of claim 30, wherein the voltage filter is a low-dropout linear filter.

35. **(Original)** The transceiver module of claim 30, wherein the voltage filter is a passive filter.

36. **(Original)** The transceiver module of claim 30, wherein the voltage filter is an active filter.

37. **(Original)** The transceiver module of claim 30, wherein the voltage filter is adapted to filter harmful noise in the range from about 10Khz to about 3Mhz.

38. **(Original)** The transceiver module of claim 30, wherein the at least one IC voltage is at least one of 1.8V, 3.3V and 5 V.

39. **(Currently amended)** The transceiver module of claim 30, wherein the signal processing integrated circuit is an application specific integrated circuit.